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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ko, *et al.* Docket No.: TSM03-0615
Serial No.: 10/729,095 Art Unit: 2811
Filed: December 5, 2003 Examiner: Hung K. Vu
For: Structure and Method of a Strained Channel Transistor and a Second Semiconductor Component in an Integrated Circuit

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Date of Deposit: October 14, 2005

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Form PTO/SB/08B with 2 References Cited (1 page)
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Return Postcard

Respectfully submitted,

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Mail Stop Amendment
Commissioner for Patents
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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08B that may be considered material to the examination of the above-identified application.

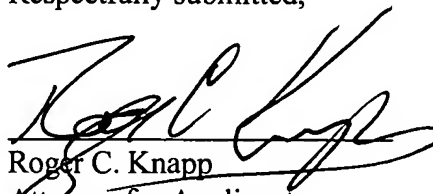
This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first. Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm. A duplicate copy of this sheet is enclosed.

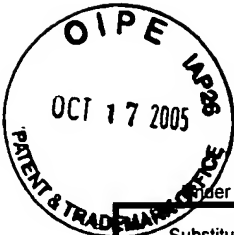
10/19/2005 FHETEKI1 00000007 501065 10729095
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October 14, 2005
Date

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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

1

Complete if Known

Application Number 10/729,095

Filing Date December 5, 2003

First Named Inventor Ko, et al.

Art Unit 2811

Examiner Name Hung K. Vu

Attorney Docket Number TSM03-0615

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	WOLF, S., et al., "Silicon Processing For The VLSI Era," Volume 1: Process Technology, Second Edition, Lattice Press, Sunset Beach, California, 2000, pp. 834-835.	
	2	WOLF, S., "Silicon Processing For The VLSI Era," Volume 2: Process Integration, Lattice Press, Sunset Beach, California, 1990, pp. 144-145.	

Examiner Signature		Date Considered	
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